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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/820,730	04/09/2004	Liang-Pin Tai	MR2707-31/DIV	9028
7590	09/07/2004		EXAMINER	
ROSENBERG, KLEIN & LEE SUITE 101 3458 ELLICOTT CENTER DRIVE ELLICOTT CITY, MD 21043			NGUYEN, TUNG X	
			ART UNIT	PAPER NUMBER
			2829	

DATE MAILED: 09/07/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)	
	10/820,730	TAI ET AL. <i>AN</i>	
	Examiner Tung X Nguyen	Art Unit 2829	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) Responsive to communication(s) filed on 09 April 2004.
- 2a) This action is FINAL.                    2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) Claim(s) 1-6 and 16-19 is/are pending in the application.
  - 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_\_ is/are allowed.
- 6) Claim(s) 1-5 and 16-19 is/are rejected.
- 7) Claim(s) 6 is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 09 April 2004 is/are: a) accepted or b) objected to by the Examiner.
 

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) All    b) Some \* c) None of:
    1. Certified copies of the priority documents have been received.
    2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | Paper No(s)/Mail Date. _____  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
|  | 6) <input type="checkbox"/> Other: _____                                    |

## DETAILED ACTION

### *Drawings*

1. Figure 6 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawing sheets are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

### *Claim Rejections - 35 USC § 103*

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 1-5, 16-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Admitted Prior Art (hereinafter APA) in Fig. 6, in view of Larson et al. (u.s.p 6,188,212).

As to claims 1, 16, APA in Fig. 6, discloses a current sense apparatus and method comprising: an output stage (72, 74 and node 20) connected a high voltage (72) and a low voltage (74) for producing a phase output current (via node 20); APA only disclose the amplifier, APA does not disclose a servo amplifier having amplifier coupling

amplifier (24, 18), wherein the servo amplifier having an amplifier coupling to the MOSFET transistor, and coupling to a mirror current circuit (37 of figure 2) for converting the mirror current (mirror current circuit 37 of figure 2) to a current sense signal (via the mirror current circuit 37 of figure 2). It would have been obvious to a person having ordinary skill in the art at the time the invention was made to modify the system of APA, and replace the amplifier by the servo amplifier, as taught by Larson et al., for detecting the current sense signal produced by the mirror current circuit (37).

As to claims 2, 4, 17-19, APA in Fig. 6, discloses a current sense apparatus wherein the output stage comprising: a phase output node (20); a common drain DMOSFET (72) connected between the high voltage (VIN) and phase output node (20); and a MOSFET connected between the low voltage (Vgnd) and phase output node (20).

As to claims 3, 6, APA in Fig. 6, discloses a DMOSFET has a gate (connect to 40), a drain (connect to VIN) coupling to a high voltage/low voltage (VIN/Vgnd), and a source (connect to 20) coupling to the phase output node (20); and Larson et al. disclose a current mirror circuit (37 of figure 2) for providing the mirror current, and a sense terminal (18A) for providing the sense voltage (coupling to 30).

#### ***Allowable Subject Matter***

4. Claim 6 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

As to claim 6, the prior art does not teach or suggest the servo amplifier having a MOSFET, wherein the inverting input of the operation amplifier connected to the source, a gate connected to the output of the operational amplifier, and a drain for providing the current sense signal; in combination with the other claimed features.

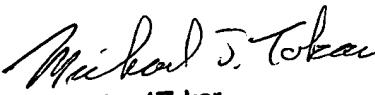
***Conclusion***

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tung X Nguyen whose telephone number is (571) 272-1967. The examiner can normally be reached on 8:30am-5:00pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TN  
8/26/04

  
Michael J. Tokar  
Supervisory Patent Examiner  
Technology Center 2800